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H-963-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

K. TAKIKAWA, et al.

Serial No. 10/664,968

Group Art Unit: 2836

Filed: September 22, 2003

Examiner: D. Nguyen

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

TRANSMITTAL OF CORRECTED FORMAL DRAWINGS


Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 3, 2005

Sir:

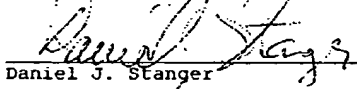
Pursuant to the Office Action dated November 3, 2004, the Applicants submit herewith six (6) sheets of corrected formal drawings (Figs. 9-12 and 13A-13B). Please substitute the corrected sheets for the originally-filed sheets of drawings.

Respectfully submitted,


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CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this Transmittal and the corrected drawings are being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below:


Daniel J. Stanger

3/3/05
Date